

Product Specification

M270HTN01.1

AU OPTRONICS CORPORATION

() Preliminary Specification

(V) Final Specification

Module	27" Color TFT-LCD
Model Name	M270HTN01.1

Customer

Date

Approved by

Note: This Specification is subject to
change without notice.

Approved by

Date

Howard Lee

Jul. 03, 2013

Prepared by

Date

Evelyn Sung

Jul. 03, 2013

AU Optronics corporation

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Record of Revision

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data)</td></tr><tr><td>10</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D13 data)</td></tr><tr><td>11</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D14 data)</td></tr><tr><td>12</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D15 data)</td></tr><tr><td>13</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D16 data)</td></tr><tr><td>14</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D17 data)</td></tr><tr><td>15</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D18 data)</td></tr><tr><td>16</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D19 data)</td></tr><tr><td>17</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D20 data)</td></tr><tr><td>18</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D21 data)</td></tr><tr><td>19</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D22 data)</td></tr><tr><td>20</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D23 data)</td></tr><tr><td>21</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D24 data)</td></tr><tr><td>22</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D25 data)</td></tr><tr><td>23</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D26 data)</td></tr><tr><td>24</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D27 data)</td></tr><tr><td>25</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D28 data)</td></tr><tr><td>26</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D29 data)</td></tr><tr><td>27</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D30 data)</td></tr><tr><td>28</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D31 data)</td></tr><tr><td>29</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D32 data)</td></tr><tr><td>30</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D33 data)</td></tr><tr><td>31</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D34 data)</td></tr><tr><td>32</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D35 data)</td></tr><tr><td>33</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D36 data)</td></tr><tr><td>34</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D37 data)</td></tr><tr><td>35</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D38 data)</td></tr><tr><td>36</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D39 data)</td></tr><tr><td>37</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D40 data)</td></tr><tr><td>38</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D41 data)</td></tr><tr><td>39</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D42 data)</td></tr><tr><td>40</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D43 data)</td></tr><tr><td>41</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D44 data)</td></tr><tr><td>42</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D45 data)</td></tr><tr><td>43</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D46 data)</td></tr><tr><td>44</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D47 data)</td></tr><tr><td>45</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D48 data)</td></tr><tr><td>46</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D49 data)</td></tr><tr><td>47</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D50 data)</td></tr><tr><td>48</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D51 data)</td></tr><tr><td>49</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D52 data)</td></tr><tr><td>50</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D53 data)</td></tr><tr><td>51</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D54 data)</td></tr><tr><td>52</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D55 data)</td></tr><tr><td>53</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D56 data)</td></tr><tr><td>54</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D57 data)</td></tr><tr><td>55</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D58 data)</td></tr><tr><td>56</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D59 data)</td></tr><tr><td>57</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D60 data)</td></tr><tr><td>58</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D61 data)</td></tr><tr><td>59</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D62 data)</td></tr><tr><td>60</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D63 data)</td></tr><tr><td>61</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D64 data)</td></tr><tr><td>62</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D65 data)</td></tr><tr><td>63</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D66 data)</td></tr><tr><td>64</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D67 data)</td></tr><tr><td>65</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D68 data)</td></tr><tr><td>66</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D69 data)</td></tr><tr><td>67</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D70 data)</td></tr><tr><td>68</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D71 data)</td></tr><tr><td>69</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D72 data)</td></tr><tr><td>70</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D73 data)</td></tr><tr><td>71</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D74 data)</td></tr><tr><td>72</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D75 data)</td></tr><tr><td>73</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D76 data)</td></tr><tr><td>74</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D77 data)</td></tr><tr><td>75</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D78 data)</td></tr><tr><td>76</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D79 data)</td></tr><tr><td>77</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D80 data)</td></tr><tr><td>78</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D81 data)</td></tr><tr><td>79</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D82 data)</td></tr><tr><td>80</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D83 data)</td></tr><tr><td>81</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D84 data)</td></tr><tr><td>82</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D85 data)</td></tr><tr><td>83</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D86 data)</td></tr><tr><td>84</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D87 data)</td></tr><tr><td>85</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D88 data)</td></tr><tr><td>86</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D89 data)</td></tr><tr><td>87</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D90 data)</td></tr><tr><td>88</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D91 data)</td></tr><tr><td>89</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D92 data)</td></tr><tr><td>90</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D93 data)</td></tr><tr><td>91</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D94 data)</td></tr><tr><td>92</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D95 data)</td></tr><tr><td>93</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D96 data)</td></tr><tr><td>94</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D97 data)</td></tr><tr><td>95</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D98 data)</td></tr><tr><td>96</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D99 data)</td></tr><tr><td>97</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D100 data)</td></tr><tr><td>98</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D101 data)</td></tr><tr><td>99</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D102 data)</td></tr><tr><td>100</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D103 data)</td></tr><tr><td>101</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D104 data)</td></tr><tr><td>102</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D105 data)</td></tr><tr><td>103</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D106 data)</td></tr><tr><td>104</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D107 data)</td></tr><tr><td>105</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D108 data)</td></tr><tr><td>106</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D109 data)</td></tr><tr><td>107</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D110 data)</td></tr><tr><td>108</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D111 data)</td></tr><tr><td>109</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D112 data)</td></tr><tr><td>110</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D113 data)</td></tr><tr><td>111</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D114 data)</td></tr><tr><td>112</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D115 data)</td></tr><tr><td>113</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D116 data)</td></tr><tr><td>114</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D117 data)</td></tr><tr><td>115</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D118 data)</td></tr><tr><td>116</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D119 data)</td></tr><tr><td>117</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D120 data)</td></tr><tr><td>118</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D121 data)</td></tr><tr><td>119</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D122 data)</td></tr><tr><td>120</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D123 data)</td></tr><tr><td>121</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D124 data)</td></tr><tr><td>122</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D125 data)</td></tr><tr><td>123</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D126 data)</td></tr><tr><td>124</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D127 data)</td></tr><tr><td>125</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D128 data)</td></tr><tr><td>126</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D129 data)</td></tr><tr><td>127</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D130 data)</td></tr><tr><td>128</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D131 data)</td></tr><tr><td>129</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D132 data)</td></tr><tr><td>130</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D133 data)</td></tr><tr><td>131</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D134 data)</td></tr><tr><td>132</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D135 data)</td></tr><tr><td>133</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D136 data)</td></tr><tr><td>134</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D137 data)</td></tr><tr><td>135</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D138 data)</td></tr><tr><td>136</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D139 data)</td></tr><tr><td>137</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D140 data)</td></tr><tr><td>138</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D141 data)</td></tr><tr><td>139</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D142 data)</td></tr><tr><td>140</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D143 data)</td></tr><tr><td>141</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D144 data)</td></tr><tr><td>142</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D145 data)</td></tr><tr><td>143</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D146 data)</td></tr><tr><td>144</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D147 data)</td></tr><tr><td>145</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D148 data)</td></tr><tr><td>146</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D149 data)</td></tr><tr><td>147</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D150 data)</td></tr><tr><td>148</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D151 data)</td></tr><tr><td>149</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D152 data)</td></tr><tr><td>150</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D153 data)</td></tr><tr><td>151</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D154 data)</td></tr><tr><td>152</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D155 data)</td></tr><tr><td>153</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D156 data)</td></tr><tr><td>154</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D157 data)</td></tr><tr><td>155</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D158 data)</td></tr><tr><td>156</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D159 data)</td></tr><tr><td>157</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D160 data)</td></tr><tr><td>158</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D161 data)</td></tr><tr><td>159</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D162 data)</td></tr><tr><td>160</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D163 data)</td></tr><tr><td>161</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D164 data)</td></tr><tr><td>162</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D165 data)</td></tr><tr><td>163</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D166 data)</td></tr><tr><td>164</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D167 data)</td></tr><tr><td>165</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D168 data)</td></tr><tr><td>166</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D169 data)</td></tr><tr><td>167</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D170 data)</td></tr><tr><td>168</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D171 data)</td></tr><tr><td>169</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D172 data)</td></tr><tr><td>170</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D173 data)</td></tr><tr><td>171</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D174 data)</td></tr><tr><td>172</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D175 data)</td></tr><tr><td>173</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D176 data)</td></tr><tr><td>174</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D177 data)</td></tr><tr><td>175</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D178 data)</td></tr><tr><td>176</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D179 data)</td></tr><tr><td>177</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D180 data)</td></tr><tr><td>178</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D181 data)</td></tr><tr><td>179</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D182 data)</td></tr><tr><td>180</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D183 data)</td></tr><tr><td>181</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D184 data)</td></tr><tr><td>182</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D185 data)</td></tr><tr><td>183</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D186 data)</td></tr><tr><td>184</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D187 data)</td></tr><tr><td>185</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D188 data)</td></tr><tr><td>186</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D189 data)</td></tr><tr><td>187</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D190 data)</td></tr><tr><td>188</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D191 data)</td></tr><tr><td>189</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D192 data)</td></tr><tr><td>190</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D193 data)</td></tr><tr><td>191</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D194 data)</td></tr><tr><td>192</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D195 data)</td></tr><tr><td>193</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D196 data)</td></tr><tr><td>194</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D197 data)</td></tr><tr><td>195</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D198 data)</td></tr><tr><td>196</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D199 data)</td></tr><tr><td>197</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D200 data)</td></tr><tr><td>198</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D201 data)</td></tr><tr><td>199</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D202 data)</td></tr><tr><td>200</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D203 data)</td></tr><tr><td>201</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D204 data)</td></tr><tr><td>202</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D205 data)</td></tr><tr><td>203</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D206 data)</td></tr><tr><td>204</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D207 data)</td></tr><tr><td>205</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D208 data)</td></tr><tr><td>206</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D209 data)</td></tr><tr><td>207</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D210 data)</td></tr><tr><td>208</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D211 data)</td></tr><tr><td>209</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D212 data)</td></tr><tr><td>210</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D213 data)</td></tr><tr><td>211</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D214 data)</td></tr><tr><td>212</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D215 data)</td></tr><tr><td>213</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D216 data)</td></tr><tr><td>214</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D217 data)</td></tr><tr><td>215</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D218 data)</td></tr><tr><td>216</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D219 data)</td></tr><tr><td>217</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D220 data)</td></tr><tr><td>218</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D221 data)</td></tr><tr><td>219</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D222 data)</td></tr><tr><td>220</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D223 data)</td></tr><tr><td>221</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D224 data)</td></tr><tr><td>222</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D225 data)</td></tr><tr><td>223</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D226 data)</td></tr><tr><td>224</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D227 data)</td></tr><tr><td>225</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D228 data)</td></tr><tr><td>226</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D229 data)</td></tr><tr><td>227</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D230 data)</td></tr><tr><td>228</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D231 data)</td></tr><tr><td>229</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D232 data)</td></tr><tr><td>230</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D233 data)</td></tr><tr><td>231</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D234 data)</td></tr><tr><td>232</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D235 data)</td></tr><tr><td>233</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D236 data)</td></tr><tr><td>234</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D237 data)</td></tr><tr><td>235</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D238 data)</td></tr><tr><td>236</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D239 data)</td></tr><tr><td>237</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D240 data)</td></tr><tr><td>238</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D241 data)</td></tr><tr><td>239</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D242 data)</td></tr><tr><td>240</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D243 data)</td></tr><tr><td>241</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D244 data)</td></tr><tr><td>242</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D245 data)</td></tr><tr><td>243</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D246 data)</td></tr><tr><td>244</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D247 data)</td></tr><tr><td>245</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D248 data)</td></tr><tr><td>246</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D249 data)</td></tr><tr><td>247</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D250 data)</td></tr><tr><td>248</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D251 data)</td></tr><tr><td>249</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D252 data)</td></tr><tr><td>250</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D253 data)</td></tr><tr><td>251</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D254 data)</td></tr><tr><td>252</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D255 data)</td></tr><tr><td>253</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D256 data)</td></tr><tr><td>254</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D257 data)</td></tr><tr><td>255</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D258 data)</td></tr><tr><td>256</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D259 data)</td></tr><tr><td>257</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D260 data)</td></tr><tr><td>258</td><td>TV(H)GND</td><td>P pin: Positive LVDS differential data input (D261 data)</td></tr><tr><td>259</td><td>TV(H)GND</td><td>P pin: Negative LVDS differential data input (D2</td></tr></table>	Pin number	Signal name	Description	1	TV(D)GND	P pin: Negative LVDS differential data input (D04 data)	2	TV(H)GND	P pin: Positive LVDS differential data input (D05 data)	3	TV(H)GND	P pin: Negative LVDS differential data input (D06 data)	4	TV(H)GND	P pin: Positive LVDS differential data input (D07 data)	5	TV(H)GND	P pin: Negative LVDS differential data input (D08 data)	6	TV(H)GND	P pin: Positive LVDS differential data input (D09 data)	7	TV(H)GND	P pin: Negative LVDS differential data input (D10 data)	8	TV(H)GND	P pin: Positive LVDS differential data input (D11 data)	9	TV(H)GND	P pin: Negative LVDS differential data input (D12 data)	10	TV(H)GND	P pin: Positive LVDS differential data input (D13 data)	11	TV(H)GND	P pin: Negative LVDS differential data input (D14 data)	12	TV(H)GND	P pin: Positive LVDS differential data input (D15 data)	13	TV(H)GND	P pin: Negative LVDS differential data input (D16 data)	14	TV(H)GND	P pin: Positive LVDS differential data input (D17 data)	15	TV(H)GND	P pin: Negative LVDS differential data input (D18 data)	16	TV(H)GND	P pin: Positive LVDS differential data input (D19 data)	17	TV(H)GND	P pin: Negative LVDS differential data input (D20 data)	18	TV(H)GND	P pin: Positive LVDS differential data input (D21 data)	19	TV(H)GND	P pin: Negative LVDS differential data input (D22 data)	20	TV(H)GND	P pin: Positive LVDS differential data input (D23 data)	21	TV(H)GND	P pin: Negative LVDS differential data input (D24 data)	22	TV(H)GND	P pin: Positive LVDS differential data input (D25 data)	23	TV(H)GND	P pin: Negative LVDS differential data input (D26 data)	24	TV(H)GND	P pin: Positive LVDS differential data input (D27 data)	25	TV(H)GND	P pin: Negative LVDS differential data input (D28 data)	26	TV(H)GND	P pin: Positive LVDS differential data input (D29 data)	27	TV(H)GND	P pin: Negative LVDS differential data input (D30 data)	28	TV(H)GND	P pin: Positive LVDS differential data input (D31 data)	29	TV(H)GND	P pin: Negative LVDS differential data input (D32 data)	30	TV(H)GND	P pin: Positive LVDS differential data input (D33 data)	31	TV(H)GND	P pin: Negative LVDS differential data input (D34 data)	32	TV(H)GND	P pin: Positive LVDS differential data input (D35 data)	33	TV(H)GND	P pin: Negative LVDS differential data input (D36 data)	34	TV(H)GND	P pin: Positive LVDS differential data input (D37 data)	35	TV(H)GND	P pin: Negative LVDS differential data input (D38 data)	36	TV(H)GND	P pin: Positive LVDS differential data input (D39 data)	37	TV(H)GND	P pin: Negative LVDS differential data input (D40 data)	38	TV(H)GND	P pin: Positive LVDS differential data input (D41 data)	39	TV(H)GND	P pin: Negative LVDS differential data input (D42 data)	40	TV(H)GND	P pin: Positive LVDS differential data input (D43 data)	41	TV(H)GND	P pin: Negative LVDS differential data input (D44 data)	42	TV(H)GND	P pin: Positive LVDS differential data input (D45 data)	43	TV(H)GND	P pin: Negative LVDS differential data input (D46 data)	44	TV(H)GND	P pin: Positive LVDS differential data input (D47 data)	45	TV(H)GND	P pin: Negative LVDS differential data input (D48 data)	46	TV(H)GND	P pin: Positive LVDS differential data input (D49 data)	47	TV(H)GND	P pin: Negative LVDS differential data input (D50 data)	48	TV(H)GND	P pin: Positive LVDS differential data input (D51 data)	49	TV(H)GND	P pin: Negative LVDS differential data input (D52 data)	50	TV(H)GND	P pin: Positive LVDS differential data input (D53 data)	51	TV(H)GND	P pin: Negative LVDS differential data input (D54 data)	52	TV(H)GND	P pin: Positive LVDS differential data input (D55 data)	53	TV(H)GND	P pin: Negative LVDS differential data input (D56 data)	54	TV(H)GND	P pin: Positive LVDS differential data input (D57 data)	55	TV(H)GND	P pin: Negative LVDS differential data input (D58 data)	56	TV(H)GND	P pin: Positive LVDS differential data input (D59 data)	57	TV(H)GND	P pin: Negative LVDS differential data input (D60 data)	58	TV(H)GND	P pin: Positive LVDS differential data input (D61 data)	59	TV(H)GND	P pin: Negative LVDS differential data input (D62 data)	60	TV(H)GND	P pin: Positive LVDS differential data input (D63 data)	61	TV(H)GND	P pin: Negative LVDS differential data input (D64 data)	62	TV(H)GND	P pin: Positive LVDS differential data input (D65 data)	63	TV(H)GND	P pin: Negative LVDS differential data input (D66 data)	64	TV(H)GND	P pin: Positive LVDS differential data input (D67 data)	65	TV(H)GND	P pin: Negative LVDS differential data input (D68 data)	66	TV(H)GND	P pin: Positive LVDS differential data input (D69 data)	67	TV(H)GND	P pin: Negative LVDS differential data input (D70 data)	68	TV(H)GND	P pin: Positive LVDS differential data input (D71 data)	69	TV(H)GND	P pin: Negative LVDS differential data input (D72 data)	70	TV(H)GND	P pin: Positive LVDS differential data input (D73 data)	71	TV(H)GND	P pin: Negative LVDS differential data input (D74 data)	72	TV(H)GND	P pin: Positive LVDS differential data input (D75 data)	73	TV(H)GND	P pin: Negative LVDS differential data input (D76 data)	74	TV(H)GND	P pin: Positive LVDS differential data input (D77 data)	75	TV(H)GND	P pin: Negative LVDS differential data input (D78 data)	76	TV(H)GND	P pin: Positive LVDS differential data input (D79 data)	77	TV(H)GND	P pin: Negative LVDS differential data input (D80 data)	78	TV(H)GND	P pin: Positive LVDS differential data input (D81 data)	79	TV(H)GND	P pin: Negative LVDS differential data input (D82 data)	80	TV(H)GND	P pin: Positive LVDS differential data input (D83 data)	81	TV(H)GND	P pin: Negative LVDS differential data input (D84 data)	82	TV(H)GND	P pin: Positive LVDS differential data input (D85 data)	83	TV(H)GND	P pin: Negative LVDS differential data input (D86 data)	84	TV(H)GND	P pin: Positive LVDS differential data input (D87 data)	85	TV(H)GND	P pin: Negative LVDS differential data input (D88 data)	86	TV(H)GND	P pin: Positive LVDS differential data input (D89 data)	87	TV(H)GND	P pin: Negative LVDS differential data input (D90 data)	88	TV(H)GND	P pin: Positive LVDS differential data input (D91 data)	89	TV(H)GND	P pin: Negative LVDS differential data input (D92 data)	90	TV(H)GND	P pin: Positive LVDS differential data input (D93 data)	91	TV(H)GND	P pin: Negative LVDS differential data input (D94 data)	92	TV(H)GND	P pin: Positive LVDS differential data input (D95 data)	93	TV(H)GND	P pin: Negative LVDS differential data input (D96 data)	94	TV(H)GND	P pin: Positive LVDS differential data input (D97 data)	95	TV(H)GND	P pin: Negative LVDS differential data input (D98 data)	96	TV(H)GND	P pin: Positive LVDS differential data input (D99 data)	97	TV(H)GND	P pin: Negative LVDS differential data input (D100 data)	98	TV(H)GND	P pin: Positive LVDS differential data input (D101 data)	99	TV(H)GND	P pin: Negative LVDS differential data input (D102 data)	100	TV(H)GND	P pin: Positive LVDS differential data input (D103 data)	101	TV(H)GND	P pin: Negative LVDS differential data input (D104 data)	102	TV(H)GND	P pin: Positive LVDS differential data input (D105 data)	103	TV(H)GND	P pin: Negative LVDS differential data input (D106 data)	104	TV(H)GND	P pin: Positive LVDS differential data input (D107 data)	105	TV(H)GND	P pin: Negative LVDS differential data input (D108 data)	106	TV(H)GND	P pin: Positive LVDS differential data input (D109 data)	107	TV(H)GND	P pin: Negative LVDS differential data input (D110 data)	108	TV(H)GND	P pin: Positive LVDS differential data input (D111 data)	109	TV(H)GND	P pin: Negative LVDS differential data input (D112 data)	110	TV(H)GND	P pin: Positive LVDS differential data input (D113 data)	111	TV(H)GND	P pin: Negative LVDS differential data input (D114 data)	112	TV(H)GND	P pin: Positive LVDS differential data input (D115 data)	113	TV(H)GND	P pin: Negative LVDS differential data input (D116 data)	114	TV(H)GND	P pin: Positive LVDS differential data input (D117 data)	115	TV(H)GND	P pin: Negative LVDS differential data input (D118 data)	116	TV(H)GND	P pin: Positive LVDS differential data input (D119 data)	117	TV(H)GND	P pin: Negative LVDS differential data input (D120 data)	118	TV(H)GND	P pin: Positive LVDS differential data input (D121 data)	119	TV(H)GND	P pin: Negative LVDS differential data input (D122 data)	120	TV(H)GND	P pin: Positive LVDS differential data input (D123 data)	121	TV(H)GND	P pin: Negative LVDS differential data input (D124 data)	122	TV(H)GND	P pin: Positive LVDS differential data input (D125 data)	123	TV(H)GND	P pin: Negative LVDS differential data input (D126 data)	124	TV(H)GND	P pin: Positive LVDS differential data input (D127 data)	125	TV(H)GND	P pin: Negative LVDS differential data input (D128 data)	126	TV(H)GND	P pin: Positive LVDS differential data input (D129 data)	127	TV(H)GND	P pin: Negative LVDS differential data input (D130 data)	128	TV(H)GND	P pin: Positive LVDS differential data input (D131 data)	129	TV(H)GND	P pin: Negative LVDS differential data input (D132 data)	130	TV(H)GND	P pin: Positive LVDS differential data input (D133 data)	131	TV(H)GND	P pin: Negative LVDS differential data input (D134 data)	132	TV(H)GND	P pin: Positive LVDS differential data input (D135 data)	133	TV(H)GND	P pin: Negative LVDS differential data input (D136 data)	134	TV(H)GND	P pin: Positive LVDS differential data input (D137 data)	135	TV(H)GND	P pin: Negative LVDS differential data input (D138 data)	136	TV(H)GND	P pin: Positive LVDS differential data input (D139 data)	137	TV(H)GND	P pin: Negative LVDS differential data input (D140 data)	138	TV(H)GND	P pin: Positive LVDS differential data input (D141 data)	139	TV(H)GND	P pin: Negative LVDS differential data input (D142 data)	140	TV(H)GND	P pin: Positive LVDS differential data input (D143 data)	141	TV(H)GND	P pin: Negative LVDS differential data input (D144 data)	142	TV(H)GND	P pin: Positive LVDS differential data input (D145 data)	143	TV(H)GND	P pin: Negative LVDS differential data input (D146 data)	144	TV(H)GND	P pin: Positive LVDS differential data input (D147 data)	145	TV(H)GND	P pin: Negative LVDS differential data input (D148 data)	146	TV(H)GND	P pin: Positive LVDS differential data input (D149 data)	147	TV(H)GND	P pin: Negative LVDS differential data input (D150 data)	148	TV(H)GND	P pin: Positive LVDS differential data input (D151 data)	149	TV(H)GND	P pin: Negative LVDS differential data input (D152 data)	150	TV(H)GND	P pin: Positive LVDS differential data input (D153 data)	151	TV(H)GND	P pin: Negative LVDS differential data input (D154 data)	152	TV(H)GND	P pin: Positive LVDS differential data input (D155 data)	153	TV(H)GND	P pin: Negative LVDS differential data input (D156 data)	154	TV(H)GND	P pin: Positive LVDS differential data input (D157 data)	155	TV(H)GND	P pin: Negative LVDS differential data input (D158 data)	156	TV(H)GND	P pin: Positive LVDS differential data input (D159 data)	157	TV(H)GND	P pin: Negative LVDS differential data input (D160 data)	158	TV(H)GND	P pin: Positive LVDS differential data input (D161 data)	159	TV(H)GND	P pin: Negative LVDS differential data input (D162 data)	160	TV(H)GND	P pin: Positive LVDS differential data input (D163 data)	161	TV(H)GND	P pin: Negative LVDS differential data input (D164 data)	162	TV(H)GND	P pin: Positive LVDS differential data input (D165 data)	163	TV(H)GND	P pin: Negative LVDS differential data input (D166 data)	164	TV(H)GND	P pin: Positive LVDS differential data input (D167 data)	165	TV(H)GND	P pin: Negative LVDS differential data input (D168 data)	166	TV(H)GND	P pin: Positive LVDS differential data input (D169 data)	167	TV(H)GND	P pin: Negative LVDS differential data input (D170 data)	168	TV(H)GND	P pin: Positive LVDS differential data input (D171 data)	169	TV(H)GND	P pin: Negative LVDS differential data input (D172 data)	170	TV(H)GND	P pin: Positive LVDS differential data input (D173 data)	171	TV(H)GND	P pin: Negative LVDS differential data input (D174 data)	172	TV(H)GND	P pin: Positive LVDS differential data input (D175 data)	173	TV(H)GND	P pin: Negative LVDS differential data input (D176 data)	174	TV(H)GND	P pin: Positive LVDS differential data input (D177 data)	175	TV(H)GND	P pin: Negative LVDS differential data input (D178 data)	176	TV(H)GND	P pin: Positive LVDS differential data input (D179 data)	177	TV(H)GND	P pin: Negative LVDS differential data input (D180 data)	178	TV(H)GND	P pin: Positive LVDS differential data input (D181 data)	179	TV(H)GND	P pin: Negative LVDS differential data input (D182 data)	180	TV(H)GND	P pin: Positive LVDS differential data input (D183 data)	181	TV(H)GND	P pin: Negative LVDS differential data input (D184 data)	182	TV(H)GND	P pin: Positive LVDS differential data input (D185 data)	183	TV(H)GND	P pin: Negative LVDS differential data input (D186 data)	184	TV(H)GND	P pin: Positive LVDS differential data input (D187 data)	185	TV(H)GND	P pin: Negative LVDS differential data input (D188 data)	186	TV(H)GND	P pin: Positive LVDS differential data input (D189 data)	187	TV(H)GND	P pin: Negative LVDS differential data input (D190 data)	188	TV(H)GND	P pin: Positive LVDS differential data input (D191 data)	189	TV(H)GND	P pin: Negative LVDS differential data input (D192 data)	190	TV(H)GND	P pin: Positive LVDS differential data input (D193 data)	191	TV(H)GND	P pin: Negative LVDS differential data input (D194 data)	192	TV(H)GND	P pin: Positive LVDS differential data input (D195 data)	193	TV(H)GND	P pin: Negative LVDS differential data input (D196 data)	194	TV(H)GND	P pin: Positive LVDS differential data input (D197 data)	195	TV(H)GND	P pin: Negative LVDS differential data input (D198 data)	196	TV(H)GND	P pin: Positive LVDS differential data input (D199 data)	197	TV(H)GND	P pin: Negative LVDS differential data input (D200 data)	198	TV(H)GND	P pin: Positive LVDS differential data input (D201 data)	199	TV(H)GND	P pin: Negative LVDS differential data input (D202 data)	200	TV(H)GND	P pin: Positive LVDS differential data input (D203 data)	201	TV(H)GND	P pin: Negative LVDS differential data input (D204 data)	202	TV(H)GND	P pin: Positive LVDS differential data input (D205 data)	203	TV(H)GND	P pin: Negative LVDS differential data input (D206 data)	204	TV(H)GND	P pin: Positive LVDS differential data input (D207 data)	205	TV(H)GND	P pin: Negative LVDS differential data input (D208 data)	206	TV(H)GND	P pin: Positive LVDS differential data input (D209 data)	207	TV(H)GND	P pin: Negative LVDS differential data input (D210 data)	208	TV(H)GND	P pin: Positive LVDS differential data input (D211 data)	209	TV(H)GND	P pin: Negative LVDS differential data input (D212 data)	210	TV(H)GND	P pin: Positive LVDS differential data input (D213 data)	211	TV(H)GND	P pin: Negative LVDS differential data input (D214 data)	212	TV(H)GND	P pin: Positive LVDS differential data input (D215 data)	213	TV(H)GND	P pin: Negative LVDS differential data input (D216 data)	214	TV(H)GND	P pin: Positive LVDS differential data input (D217 data)	215	TV(H)GND	P pin: Negative LVDS differential data input (D218 data)	216	TV(H)GND	P pin: Positive LVDS differential data input (D219 data)	217	TV(H)GND	P pin: Negative LVDS differential data input (D220 data)	218	TV(H)GND	P pin: Positive LVDS differential data input (D221 data)	219	TV(H)GND	P pin: Negative LVDS differential data input (D222 data)	220	TV(H)GND	P pin: Positive LVDS differential data input (D223 data)	221	TV(H)GND	P pin: Negative LVDS differential data input (D224 data)	222	TV(H)GND	P pin: Positive LVDS differential data input (D225 data)	223	TV(H)GND	P pin: Negative LVDS differential data input (D226 data)	224	TV(H)GND	P pin: Positive LVDS differential data input (D227 data)	225	TV(H)GND	P pin: Negative LVDS differential data input (D228 data)	226	TV(H)GND	P pin: Positive LVDS differential data input (D229 data)	227	TV(H)GND	P pin: Negative LVDS differential data input (D230 data)	228	TV(H)GND	P pin: Positive LVDS differential data input (D231 data)	229	TV(H)GND	P pin: Negative LVDS differential data input (D232 data)	230	TV(H)GND	P pin: Positive LVDS differential data input (D233 data)	231	TV(H)GND	P pin: Negative LVDS differential data input (D234 data)	232	TV(H)GND	P pin: Positive LVDS differential data input (D235 data)	233	TV(H)GND	P pin: Negative LVDS differential data input (D236 data)	234	TV(H)GND	P pin: Positive LVDS differential data input (D237 data)	235	TV(H)GND	P pin: Negative LVDS differential data input (D238 data)	236	TV(H)GND	P pin: Positive LVDS differential data input (D239 data)	237	TV(H)GND	P pin: Negative LVDS differential data input (D240 data)	238	TV(H)GND	P pin: Positive LVDS differential data input (D241 data)	239	TV(H)GND	P pin: Negative LVDS differential data input (D242 data)	240	TV(H)GND	P pin: Positive LVDS differential data input (D243 data)	241	TV(H)GND	P pin: Negative LVDS differential data input (D244 data)	242	TV(H)GND	P pin: Positive LVDS differential data input (D245 data)	243	TV(H)GND	P pin: Negative LVDS differential data input (D246 data)	244	TV(H)GND	P pin: Positive LVDS differential data input (D247 data)	245	TV(H)GND	P pin: Negative LVDS differential data input (D248 data)	246	TV(H)GND	P pin: Positive LVDS differential data input (D249 data)	247	TV(H)GND	P pin: Negative LVDS differential data input (D250 data)	248	TV(H)GND	P pin: Positive LVDS differential data input (D251 data)	249	TV(H)GND	P pin: Negative LVDS differential data input (D252 data)	250	TV(H)GND	P pin: Positive LVDS differential data input (D253 data)	251	TV(H)GND	P pin: Negative LVDS differential data input (D254 data)	252	TV(H)GND	P pin: Positive LVDS differential data input (D255 data)	253	TV(H)GND	P pin: Negative LVDS differential data input (D256 data)	254	TV(H)GND	P pin: Positive LVDS differential data input (D257 data)	255	TV(H)GND	P pin: Negative LVDS differential data input (D258 data)	256	TV(H)GND	P pin: Positive LVDS differential data input (D259 data)	257	TV(H)GND	P pin: Negative LVDS differential data input (D260 data)	258	TV(H)GND	P pin: Positive LVDS differential data input (D261 data)	259	TV(H)GND	P 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250	TV(H)GND	P pin: Positive LVDS differential data input (D253 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
251	TV(H)GND	P pin: Negative LVDS differential data input (D254 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
252	TV(H)GND	P pin: Positive LVDS differential data input (D255 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
253	TV(H)GND	P pin: Negative LVDS differential data input (D256 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
254	TV(H)GND	P pin: Positive LVDS differential data input (D257 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
255	TV(H)GND	P pin: Negative LVDS differential data input (D258 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
256	TV(H)GND	P pin: Positive LVDS differential data input (D259 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
257	TV(H)GND	P pin: Negative LVDS differential data input (D260 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
258	TV(H)GND	P pin: Positive LVDS differential data input (D261 data)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
259	TV(H)GND	P pin: Negative LVDS differential data input (D2																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													

1 Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open or modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case a TFT-LCD Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED lightbar edge. Otherwise the TFT-LCD Module may be damaged.
- 10) Insert or pull out the interface connector, be sure not to rotate nor tilt it of the TFT-LCD Module.
- 11) Do not twist nor bend the TFT -LCD Module even momentary. It should be taken into consideration that no bending/twisting forces are applied to the TFT-LCD Module from outside. Otherwise the TFT-LCD Module may be damaged.
- 12) Please avoid touching COF position while you are doing mechanical design.
- 13) When storing modules as spares for a long time, the following precaution is necessary:
Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.

2 General Description

This specification applies to the 27 inch wide Color a-Si TFT-LCD Module M270HTN01.1. The display supports the Full HD - 1920(H) x 1080(V) screen format and 16.7M colors (RGB 6-bits + Hi-FRC data). The input interface is tow channel LVDS and this module doesn't contain an driver board for backlight.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25°C condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	685.65(27")
Active Area	[mm]	597.6 (H) x 336.15 (V)
Pixels H x V	-	1920(x3) x 1080
Pixel Pitch	[um]	311.25 (per one triad) x311.25
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	TN Mode, Normally White
White Luminance (Center)	[cd/m ²]	300 (Typ.)
Contrast Ratio	-	1000:1 (Typ.)
Response Time	[msec]	5ms (Typ., on/off)
Power Consumption (LCD Module + Backligh unit)	[Watt]	25.84 (Typ.) LCD module : PDD (Typ.)=5.5 @ Black pattern,Fv=75Hz Backlight unit : P _{BLU} (Typ.) =20.34 @Is=120mA
Weight	[Grams]	2860
Outline Dimension	[mm]	630 (W) x 368.2 (H) x 14.1(D)
Electrical Interface	-	Two channel LVDS
Support Color	-	16.7M colors (RGB 6-bit + Hi_FRC)
Surface Treatment	-	Anti-Glare, 3H
Temperature Range		
Operating	[°C]	0 to +50
Storage (Shipping)	[°C]	-20 to +60
RoHS Compliance	-	RoHS Compliance
TCO Compliance	-	TCO 6.0 Compliance

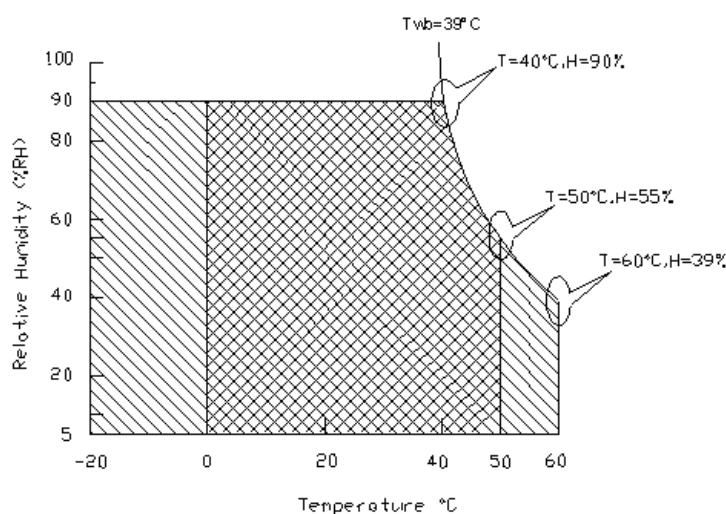
2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	Note 2-1
TGS	Glass surface temperature (operation)	0	+65	[°C]	Note 2-1 Function judged only
HOP	Operation Humidity	5	90	[%RH]	Note 2-1
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

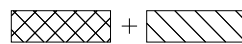
1. 90% RH Max ($T_a \leq 39^\circ\text{C}$)
2. Max wet-bulb temperature at 39°C or less. ($T_a \leq 39^\circ\text{C}$)
3. No condensation



Operating Range



Storage Range



2.3 Optical Characteristics

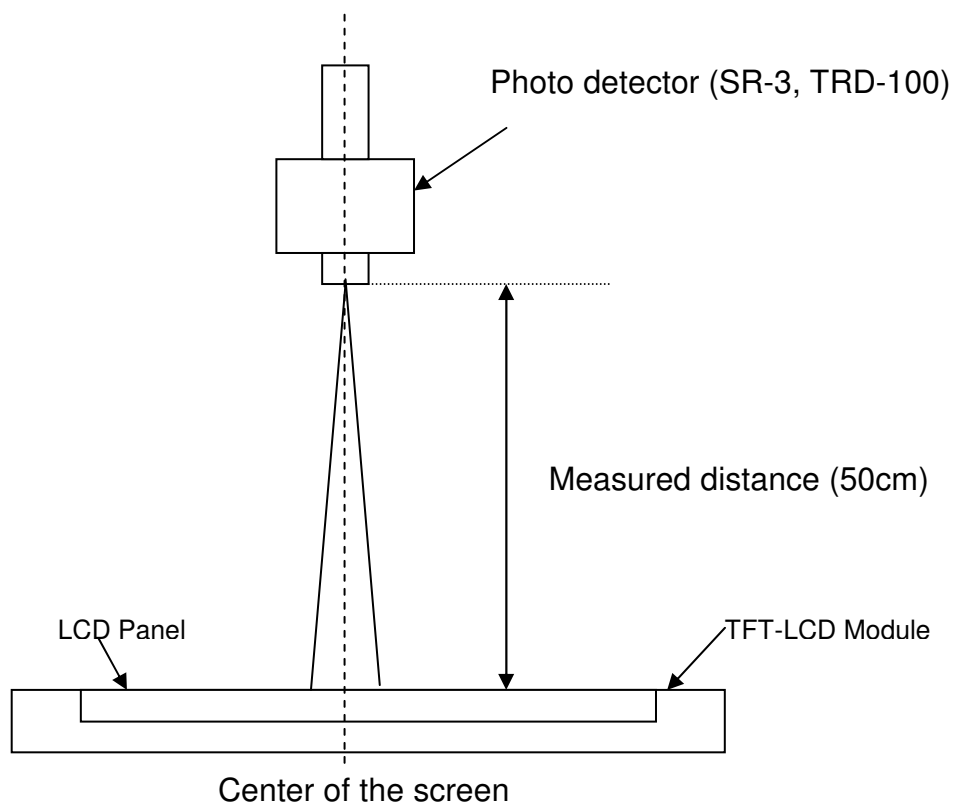
The optical characteristics are measured on the following test condition.

Test Condition:

1. Equipment setup: Please refer to **Note 2-2**.
2. Panel Lighting time: 30 minutes
3. VDD=5.0V, Fv=75Hz, Is=120mA, Ta=25 °C

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
L _w	White Luminance (Center of screen)		240	300	-	[cd/m ²]	Note 2-2 By SR-3
L _{uni}	Luminance Uniformity (9 points)		75	80	-	[%]	Note 2-3 By SR-3
CR	Contrast Ratio (Center of screen)		600	1000	-	-	Note 2-4 By SR-3
θ _R	Horizontal Viewing Angle (CR=10)	Right	75	85	-	[degree]	Note 2-5 By SR-3
θ _L		Left	75	85	-		
Φ _H	Vertical Viewing Angle (CR=10)	Up	70	80	-		
Φ _L		Down	70	80	-		
θ _R	Horizontal Viewing Angle (CR=5)	Right	75	88	-		
θ _L		Left	75	88	-		
Φ _H	Vertical Viewing Angle (CR=5)	Up	70	85	-		
Φ _L		Down	70	85	-		
T _R	Response Time	Rising Time	-	3.8	5.5	[msec]	Note 2-6 By TRD-100
T _F		Falling Time	-	1.2	2.5		
-		Rising + Falling	-	5	8		
R _x	Color Coordinates (CIE 1931)	Red x	0.617	0.647	0.677	-	By SR-3
R _y		Red y	0.312	0.342	0.372		
G _x		Green x	0.288	0.318	0.348		
G _y		Green y	0.601	0.631	0.661		
B _x		Blue x	0.120	0.150	0.180		
B _y		Blue y	0.020	0.050	0.080		
W _x		White x	0.283	0.313	0.343		
W _y		White y	0.299	0.329	0.359		
CT	Crosstalk		-	-	1.5	[%]	Note 2-7 By SR-3
F _{dB}	Flicker (Center of screen)		-	-	-20	[dB]	Note 2-8 By SR-3

Note 2-2: Equipment setup :

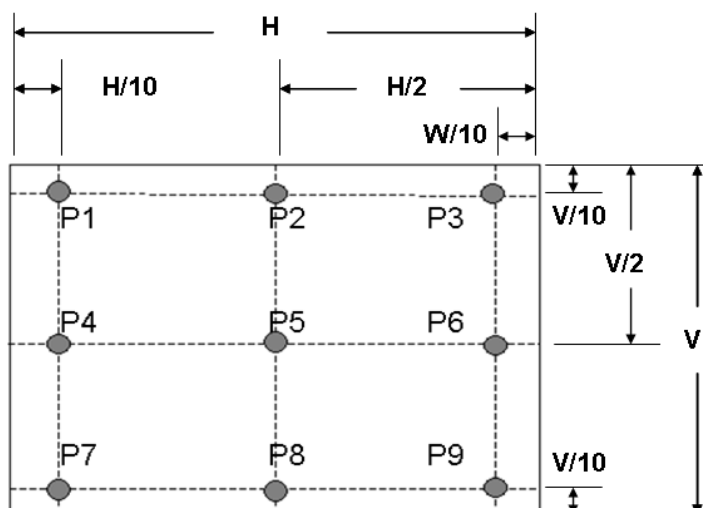


Note 2-3: Luminance Uniformity Measurement

Definition:

$$\text{Luminance Uniformity} = \frac{\text{Minimum Luminance of 9 Points (P1 ~ P9)}}{\text{Maximum Luminance of 9 Points (P1 ~ P9)}}$$

a. Test pattern: White Pattern



Note 2-4: Contrast Ratio Measurement

Definition:

$$\text{Contrast Ratio} = \frac{\text{Luminance of White pattern}}{\text{Luminance of Black pattern}}$$

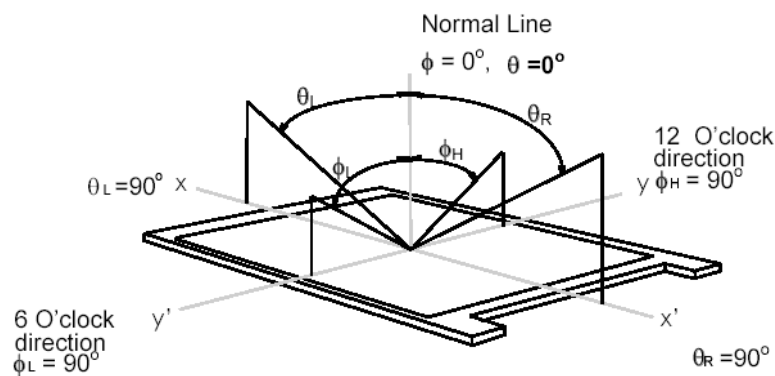
- a. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta = \Phi = 0^\circ$)

Note 2-5: Viewing angle measurement

Definition: The angle at which the contrast ratio is greater than 10 & 5 .

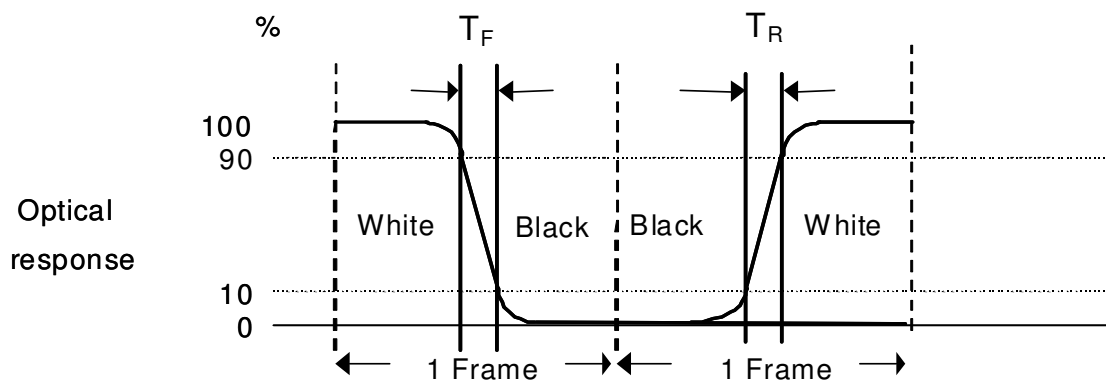
- a. Horizontal view angle: Divide to left & right (θ_L & θ_R)

Vertical view angle: Divide to up & down (Φ_H & Φ_L)



Note 2-6: Response time measurement

The output signals of photo detector are measured when the input signals are changed from “Black” to “White” (rising time, T_R), and from “White” to “Black” (falling time, T_F), respectively. The response time is interval between the 10% and 90% of optical response. (*Black & White color definition: Please refer section 3.4.3*)



Note 2-7: Crosstalk measurement

Definition:

$$CT = \text{Max. } (CT_H, CT_V);$$

Where

a. Maximum Horizontal Crosstalk :

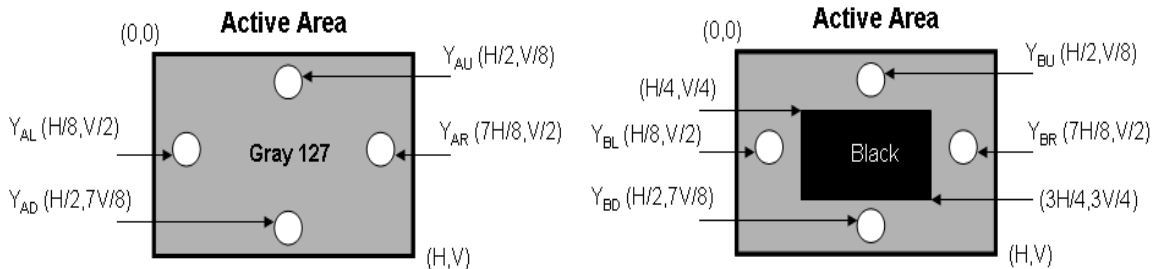
$$CT_H = \text{Max. } (|Y_{BL} - Y_{AL}| / Y_{AL} \times 100 \%, |Y_{BR} - Y_{AR}| / Y_{AR} \times 100 \%);$$

Maximum Vertical Crosstalk:

$$CT_V = \text{Max. } (|Y_{BU} - Y_{AU}| / Y_{AU} \times 100 \%, |Y_{BD} - Y_{AD}| / Y_{AD} \times 100 \%);$$

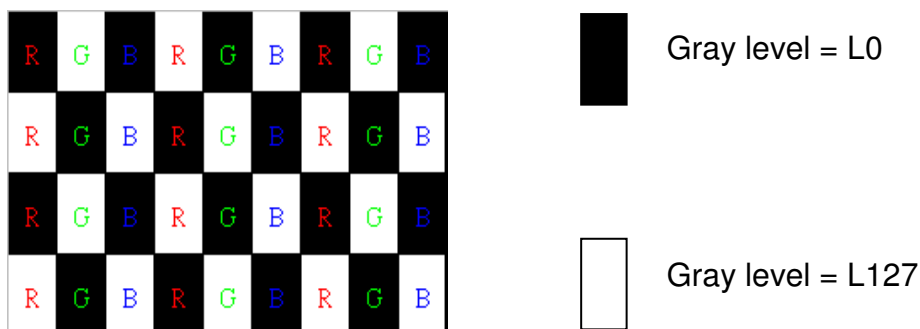
b. Y_{AU} , Y_{AD} , Y_{AL} , Y_{AR} = Luminance of measured location without Black pattern

Y_{BU} , Y_{BD} , Y_{BL} , Y_{BR} = Luminance of measured location with Black pattern



Note 2-8: Flicker measurement

a. Test pattern: It is listed as following.



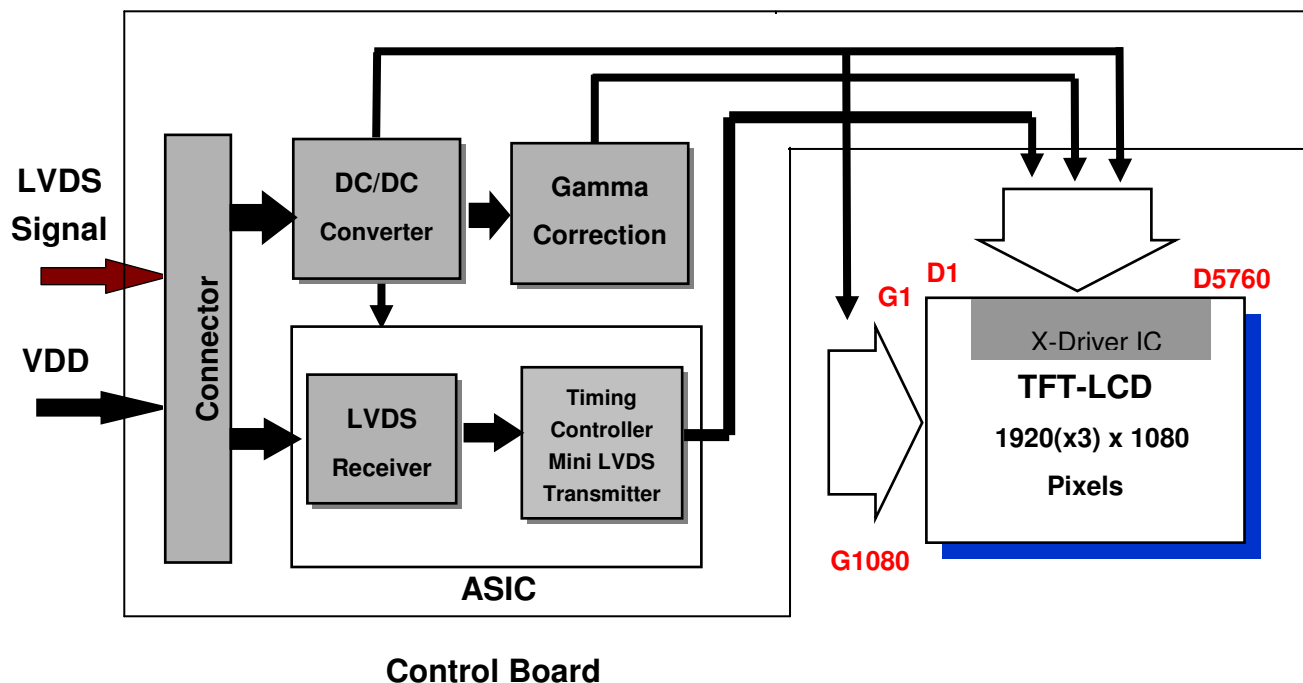
R: Red, G: Green, B:Blue

b. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta=\Phi=0^\circ$)

3 TFT-LCD Module

3.1 Block Diagram

The following shows the block diagram of the 27 inch Color TFT-LCD Module.



3.2 Interface Connection

3.2.1 Connector Type

TFT-LCD Connector	Manufacturer	P-Two	STM
	Part Number	AL230F-A0G1D-P	MSCKT2407P30HB
Mating Connector	Manufacturer	JAE	
	Part Number	FI-X30HL (Locked Type)	

3.2.2 Connector Pin Assignment

PIN #	Symbol	Description
1	RxO0-	Negative LVDS differential data input (Odd data)
2	RxO0+	Positive LVDS differential data input (Odd data)
3	RxO1-	Negative LVDS differential data input (Odd data)
4	RxO1+	Positive LVDS differential data input (Odd data)
5	RxO2-	Negative LVDS differential data input (Odd data)
6	RxO2+	Positive LVDS differential data input (Odd data)
7	GND	Ground
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)
10	RxO3-	Negative LVDS differential data input (Odd data)
11	RxO3+	Positive LVDS differential data input (Odd data)
12	RxE0-	Negative LVDS differential data input (Even data)
13	RxE0+	Positive LVDS differential data input (Even data)
14	GND	Ground
15	RxE1-	Negative LVDS differential data input (Even data)
16	RxE1+	Positive LVDS differential data input (Even data)
17	GND	Ground
18	RxE2-	Negative LVDS differential data input (Even data)
19	RxE2+	Positive LVDS differential data input (Even data)
20	RxECLK-	Negative LVDS differential clock input (Even clock)
21	RxECLK+	Positive LVDS differential clock input (Even clock)
22	RxE3-	Negative LVDS differential data input (Even data)
23	RxE3+	Positive LVDS differential data input (Even data)
24	GND	Ground
25	NC	No connection (for AUO test only. Do not connect)



Product Specification

M270HTN01.1

AU OPTRONICS CORPORATION

26	NC	No connection (for AUO test only. Do not connect)
27	NC	No connection (for AUO test only. Do not connect)
28	VDD	Power Supply Input Voltage
29	VDD	Power Supply Input Voltage
30	VDD	Power Supply Input Voltage

3.3 Electrical Characteristics

3.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

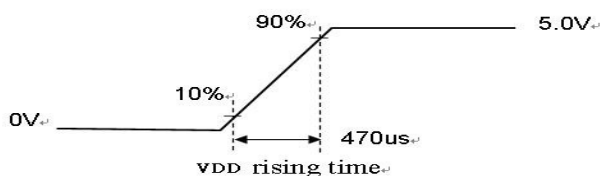
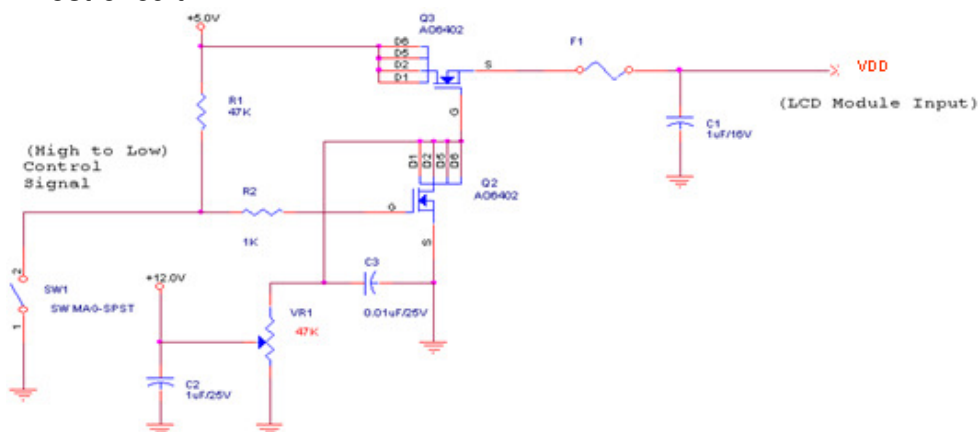
Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25 °C

3.3.2 Recommended Operating Condition

Symbol	Description	Min	Typ	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
IDD	Power supply Input Current (RMS)	-	0.9	1.1	[A]	VDD= 5.0V, Black Pattern, Fv=60Hz
			1.1	1.3	[A]	VDD= 5.0V, Black Pattern, Fv=75Hz
PDD	VDD Power Consumption	-	4.5	5.5	[Watt]	VDD= 5.0V, Black Pattern, Fv=60Hz
			5.5	6.5	[Watt]	VDD= 5.0V, Black Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3.0	[A]	Note 3-1
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD= 5.0V, Black Pattern, Fv=75Hz

Note 3-1: Inrush Current measurement:

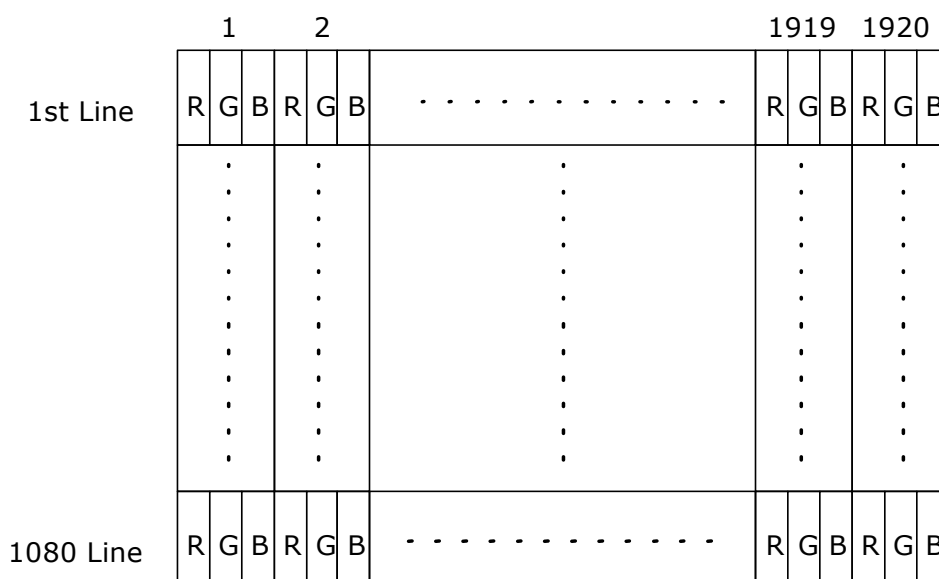
Test circuit:



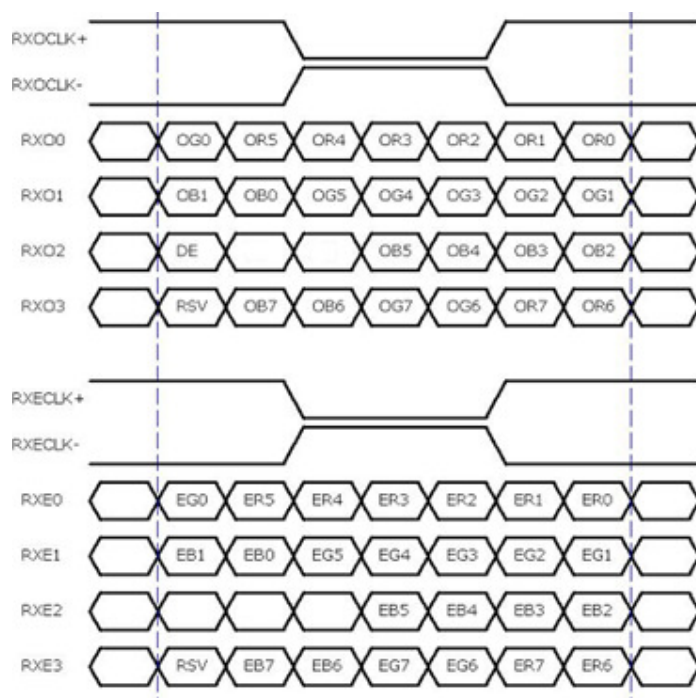
The duration of VDD rising time: 470us.

3.4 Signal Characteristics

3.4.1 LCD Pixel Format



3.4.2 LVDS Data Format



8 Bit Color Bit Order			
MSB	R7	G7	B7
	R6	G6	B6
	R5	G5	B5
	R4	G4	B4
	R3	G3	B3
	R2	G2	B2
	R1	G1	B1
LSB	R0	G0	B0

Note 3-2:

- O = "Odd Pixel Data" E = "Even Pixel Data"
- Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

Color	Gray Level	Color Input Data																								Remark
		RED data (MSB :R7, LSB :R0)								GREEN data (MSB :G7, LSB :G0)								BLUE data (MSB :B7, LSB :B0)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1		
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0		
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1		

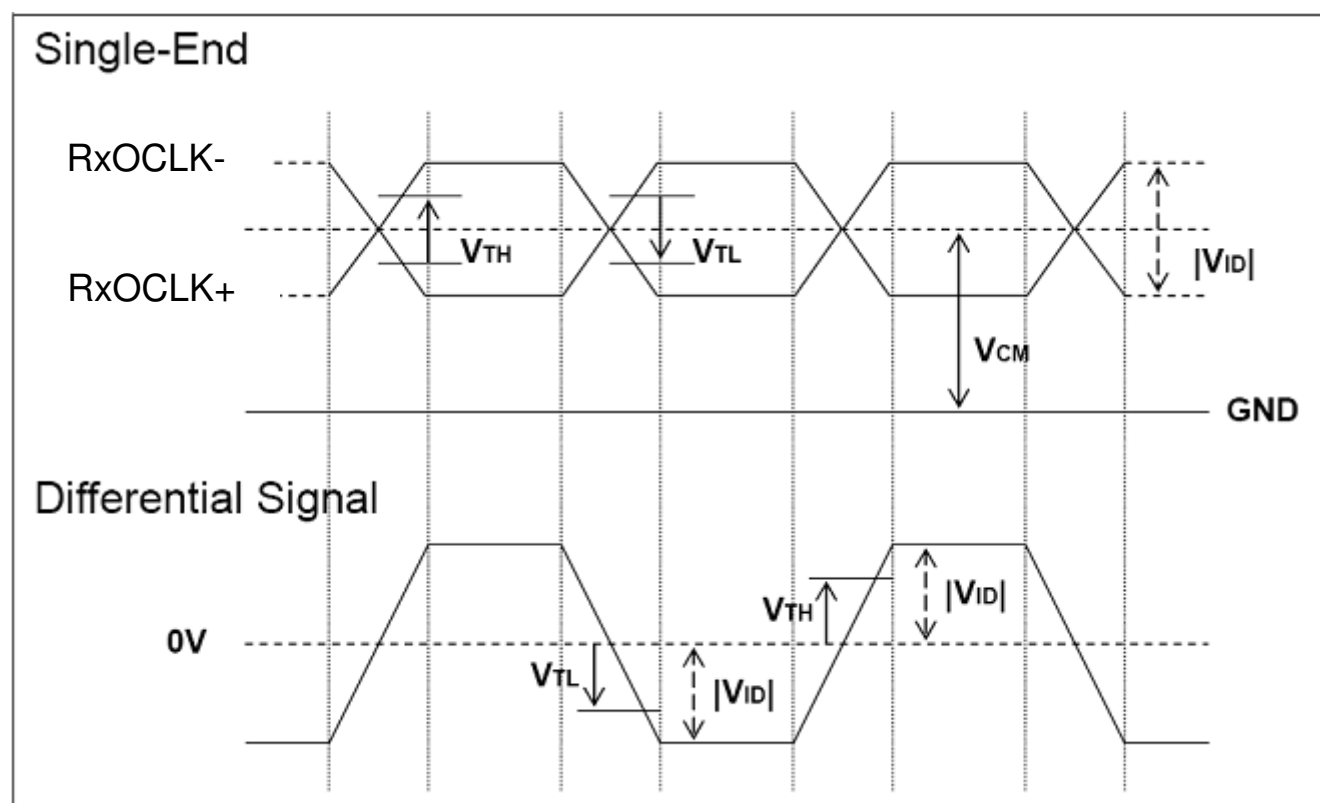
3.4.4 LVDS Specification

a. DC Characteristics:

Symbol	Description	Min	Typ	Max	Units	Condition
V_{TH}	LVDS Differential Input High Threshold	-	-	+100	[mV]	$V_{CM} = 1.2V$
V_{TL}	LVDS Differential Input Low Threshold	-100	-	-	[mV]	$V_{CM} = 1.2V$
$ V_{ID} $	LVDS Differential Input Voltage	100	-	600	[mV]	
V_{CM}	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	$V_{TH} - V_{TL} = 200mV$

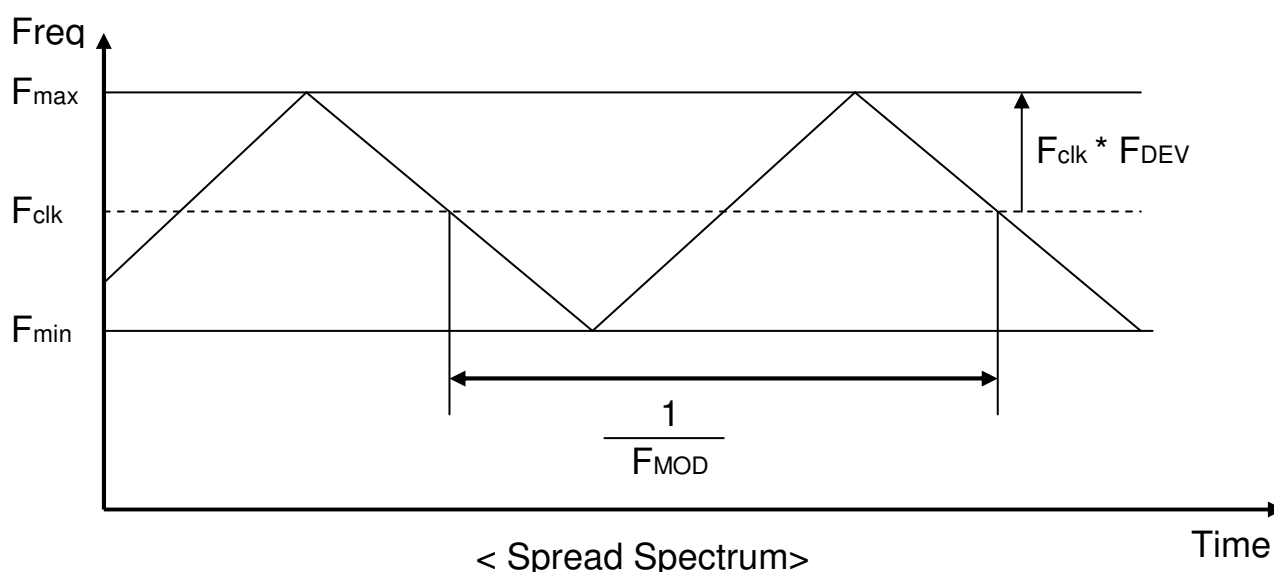
LVDS Signal Waveform:

Use RxOCLK- & RxOCLK+ as example.



b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
F_{DEV}	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



F_{clk} : LVDS Clock Frequency

3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Tv	Vertical Section	Period	1092	1130	1818	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	12	50	738	Th	
Fv		Frequency	50	60	76	Hz	
Th	Horizontal Section	Period	1034	1050	1100	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	74	90	140	Tclk	
Fh		Frequency	55	68	91	KHz	Note 3-3
Tclk	LVDS Clock	Period	10.6	14.0	17.7	ns	1/Fclk
Fclk		Frequency	56.5	71.2	94.0	MHz	Note 3-4

Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

$$Fh \text{ (Min.)} = Fclk \text{ (Min.)} / Th \text{ (Min.)};$$

$$Fh \text{ (Typ.)} = Fclk \text{ (Typ.)} / Th \text{ (Typ.)};$$

$$Fh \text{ (Max.)} = Fclk \text{ (Max.)} / Th \text{ (Min.)};$$

Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

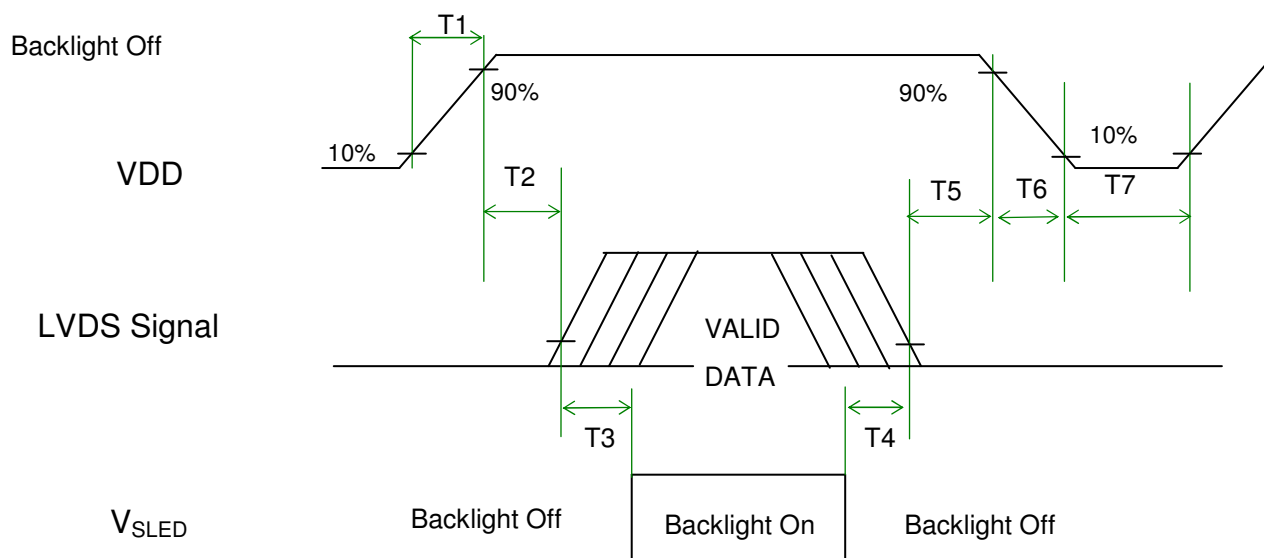
$$Fclk \text{ (Min.)} = Fv \text{ (Min.)} \times Th \text{ (Min.)} \times Tv \text{ (Min.)};$$

$$Fclk \text{ (Typ.)} = Fv \text{ (Typ.)} \times Th \text{ (Typ.)} \times Tv \text{ (Typ.)};$$

$$Fclk \text{ (Max.)} = Fv \text{ (Max.)} \times Th \text{ (Typ.)} \times Tv \text{ (Typ.)};$$

3.5 Power ON/OFF Sequence

VDD power, LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0		50	[ms]	Note 3-5 Note 3-6
T6	0	-	100	[ms]	Note 3-6
T7	1000	-	-	[ms]	

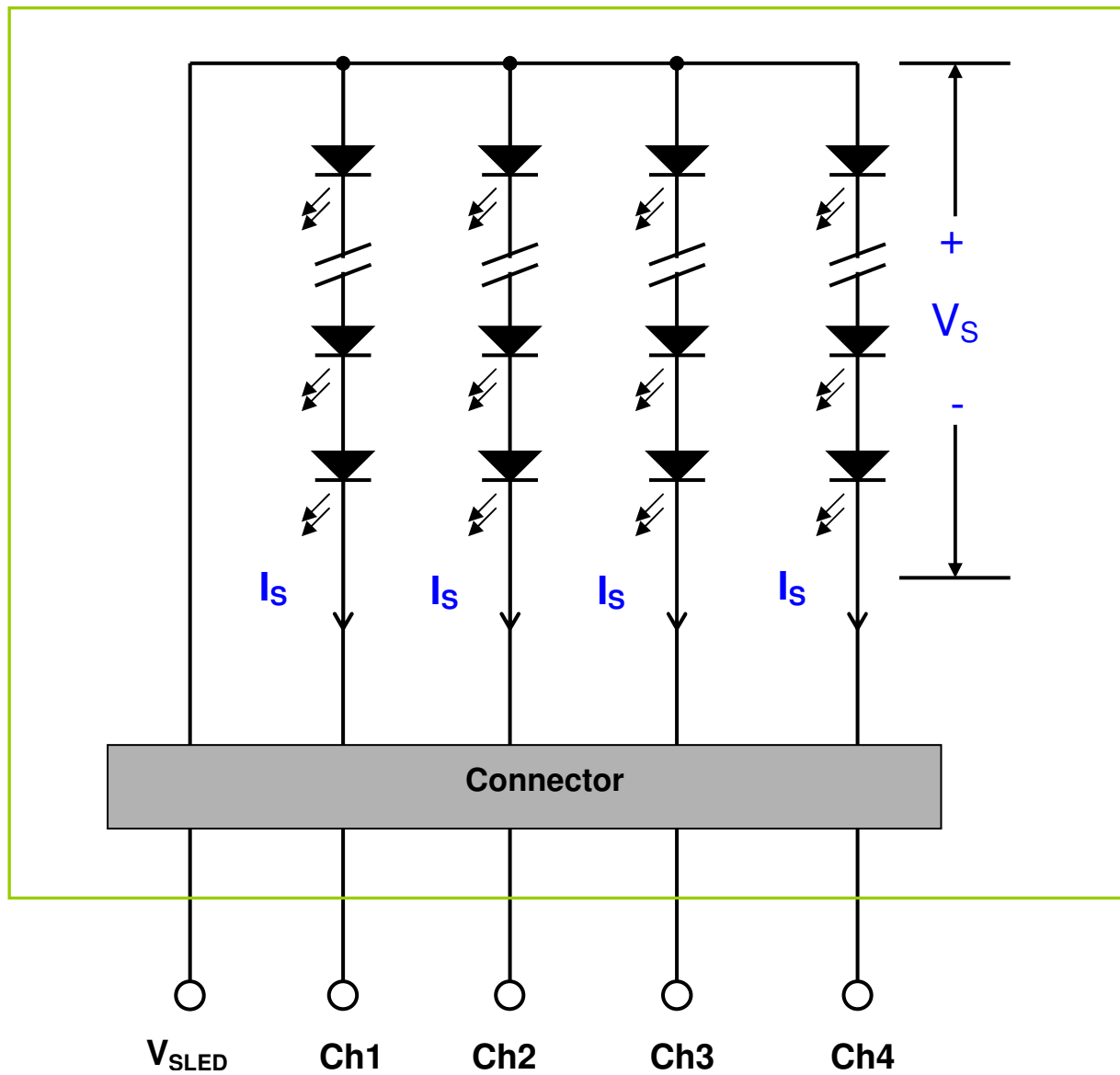
Note 3-5 : Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

Note 3-6 : During T5 and T6 period , please keep the level of input LVDS signals with Hi-Z state.

4 Backlight Unit

4.1 Block Diagram

The following shows the block diagram of the 27 inch Backlight Unit. And it includes 52 pcs LED in the LED light bar. (4 strings and 13 pcs LED of one string).



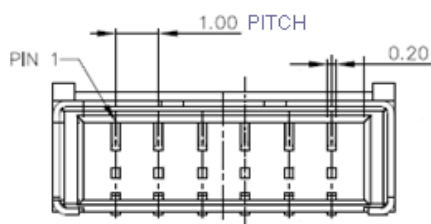
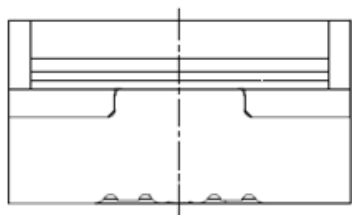
4.2 Interface Connection

4.2.1 Connector Type

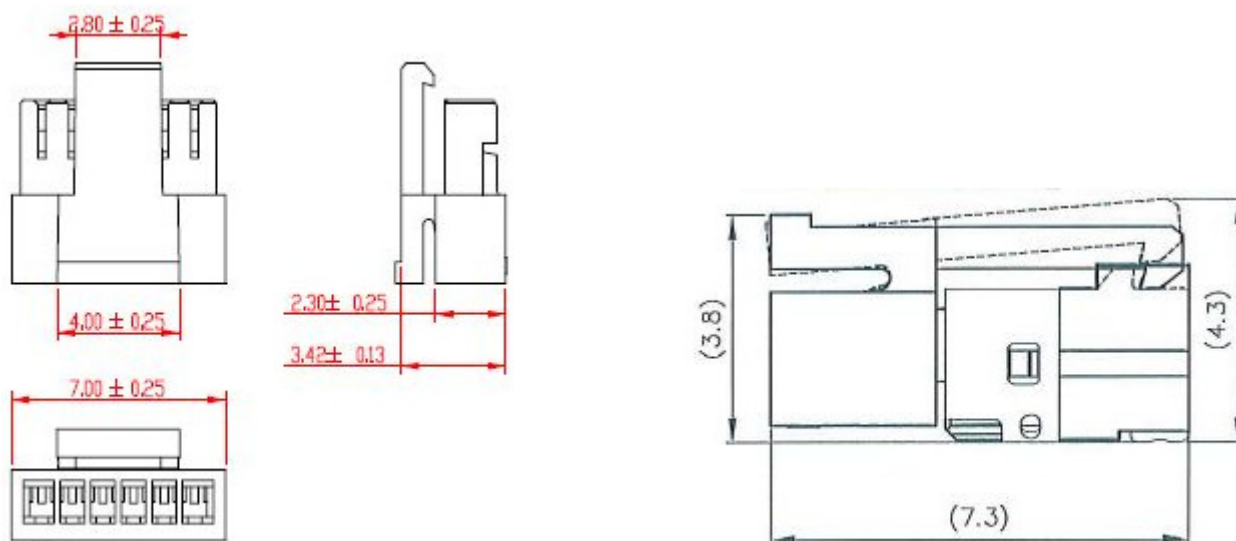
Backlight Connector	Manufacturer	ENTERY
	Part Number	3707K-S06N-21R
Mating Connector	Manufacturer	ENTERY
	Part Number	H112K-P06N-00B (Non-Locking type) H112K-P06N-03B (Locking type)

Backlight Connector dimension:

$H \times V \times D = 13.9 \times 3.00 \times 4.25$, Pitch = 1.0(unit = mm)

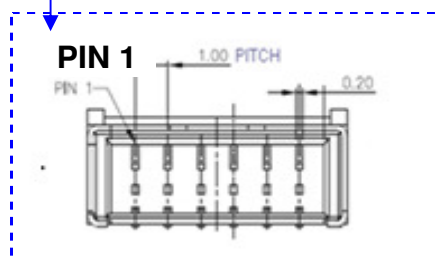
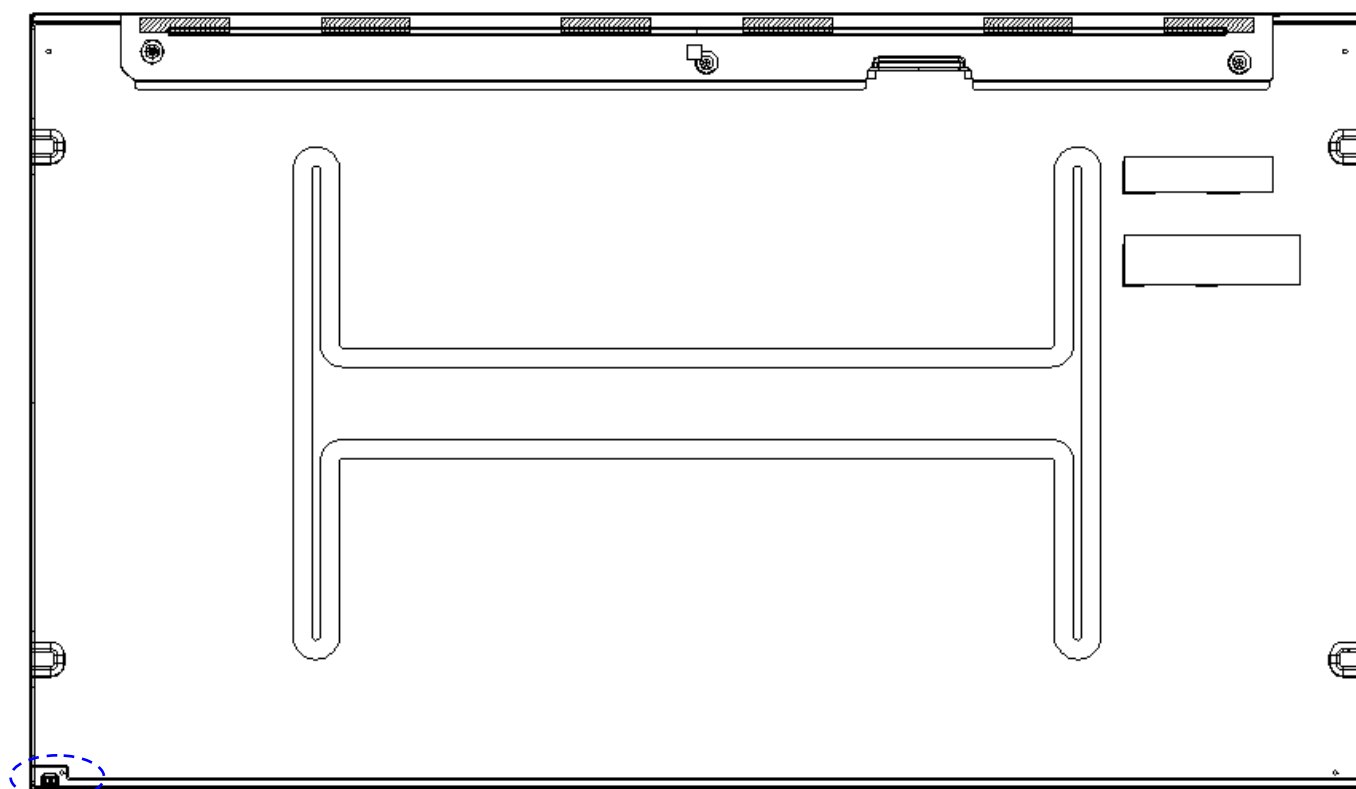


Mating Connector dimension:



4.2.2 Connector Pin Assignment

Pin#	Symbol	Description	Remark
1	Ch1	LED Current Feedback Terminal (Channel 1)	
2	Ch2	LED Current Feedback Terminal (Channel 2)	
3	V _{SLED}	LED Power Supply Voltage Input Terminal	
4	V _{SLED}	LED Power Supply Voltage Input Terminal	
5	Ch3	LED Current Feedback Terminal (Channel 3)	
6	Ch4	LED Current Feedback Terminal (Channel 4)	



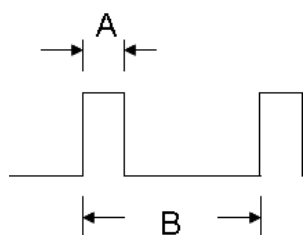
4.3 Electrical Characteristics

4.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

(Ta=25°C)

Symbol	Description	Min	Max	Unit	Remark
Is	LED String Current	0	150	[mA]	100% duty ratio
			210	[mA]	Duty ratio ≤ 10% Pulse time=10 ms



Duty ratio= (A / B) X 100% ; (A: Pulse time, B: Period)

4.3.2 Recommended Operating Condition

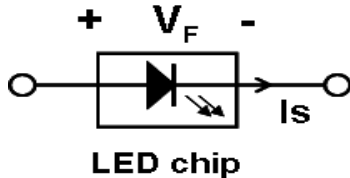
(Ta=25°C)

Symbol	Description	Min.	Typ.	Max.	Unit	Remark
Is	LED String Current	-	120	132	[mA]	100% duty ratio of LED chip
Vs	LED String Voltage	39	42.9	46.8	[Volt]	Is=120mA @ 100% duty ratio; Note 4-1, Note 4-5
ΔVs	Maximum Vs Voltage Deviation of light bar	-	-	2.6	[Volt]	Is=120mA @ 100% duty ratio; Note 4-2
P _{BLU}	LED Light Bar Power Consumption	-	20.6	22.5	[Watt]	Note 4-3
LT _{LED}	LED Life Time	30,000	-	-	[Hour]	Note 4-4

Note 4-1: $V_s (\text{Typ.}) = V_F (\text{Typ.}) \times \text{LED No. (one string)}$;

a. V_F : LED chip forward voltage, $V_F (\text{Min.})=3.0\text{V}$, $V_F (\text{Typ.})=3.3\text{V}$, $V_F (\text{Max.})=3.6\text{V}$

b. The same equation to calculate $V_s (\text{Min.})$ & $V_s (\text{Max.})$ for respective $V_F (\text{Min.})$ & $V_F (\text{Max.})$;



Note 4-2: $\Delta V_s (\text{Max.}) = \Delta V_F \times \text{LED No. (one string)}$;

a. ΔV_F : LED chip forward voltage deviation; (0.2 V , each Bin of LED V_F)

Note 4-3: $P_{\text{BLU}} (\text{Typ.}) = V_s (\text{Typ.}) \times I_s (\text{Typ.}) \times 4$; (4 is total String No. of LED Light bar)

$P_{\text{BLU}} (\text{Max.}) = V_s (\text{Max.}) \times I_s (\text{Typ.}) \times 4$;

Note 4-4: Definition of life time:

a. Brightness of LED becomes to 50% of its original value

b. Test condition: $I_s = 120\text{mA}$ and 25°C (Room Temperature)

Note 4-5: Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. AUO strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (V_s) at least.

Note 4-6: AUO strongly recommend “Analog Dimming” method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.

5 Reliability Test

AUO reliability test items are listed as following table. (*Bare Panel only*)

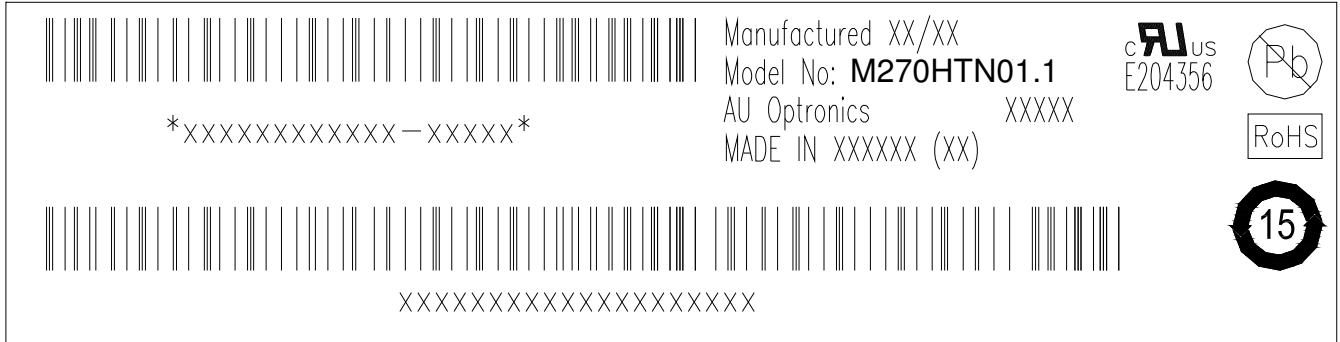
Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50 °C , 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50 °C , 50%RH, 300hours	
Low Temperature Operation (LTO)	Ta= 0 °C , 300hours	
High Temperature Storage (HTS)	Ta= 60 °C , 300hours	
Low Temperature Storage (LTS)	Ta= -20 °C , 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Drop Test	Height: 45.7 cm, package test	
Thermal Shock Test (TST)	-20 °C /30min, 60 °C /30min, 100 cycles	Note 5-1
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 15KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point.	Note 5-2
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.	
Altitude Test	Operation:18,000 ft Non-Operation:40,000 ft	

Note 5-1: a. A cycle of rapid temperature change consists of varying the temperature from -20 °C to 60 °C, and back again. Power is not applied during the test.
b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 5-2: EN61000-4-2, ESD class B: Certain performance degradation allowed
No data lost
Self-recoverable
No hardware failures.

6 Shipping Label

The label is on the panel as shown below:



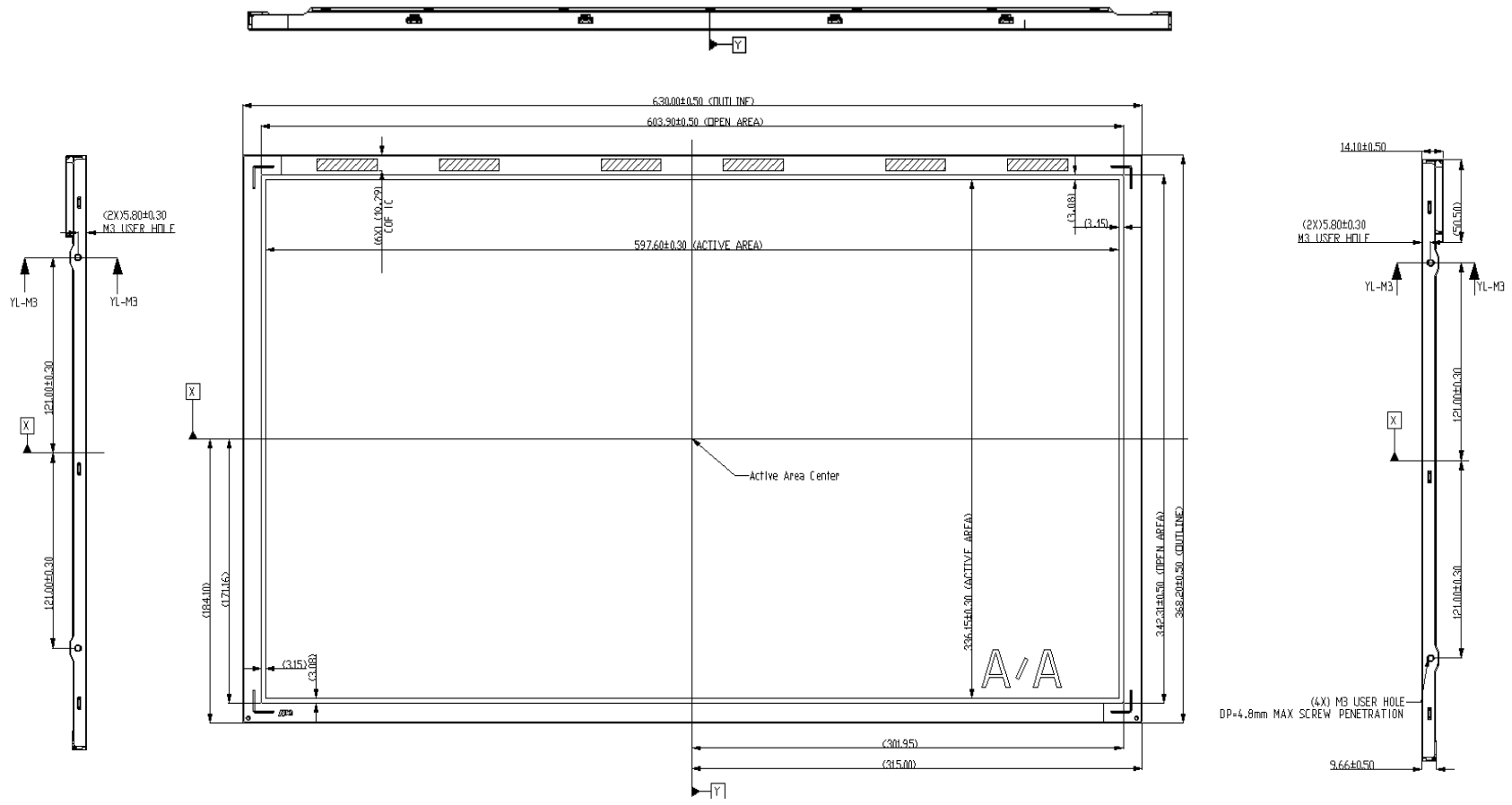
Note 6-1: For Pb Free products, AUO will add  for identification.

Note 6-2: For RoHS compatible products, AUO will add  for identification.

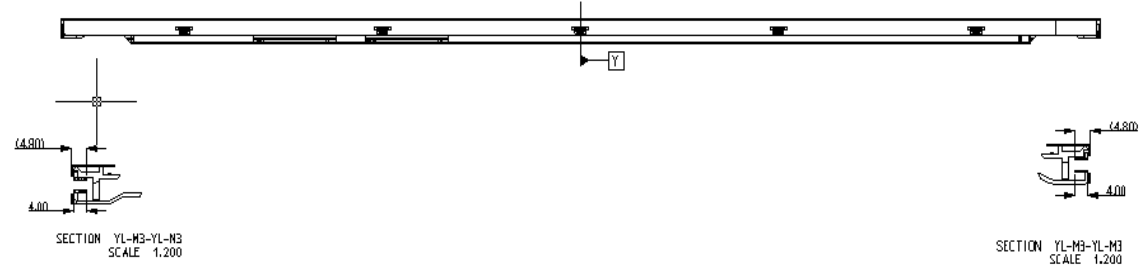
Note 6-3: For China RoHS compatible products, AUO will add  for identification.

Note 6-4: The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

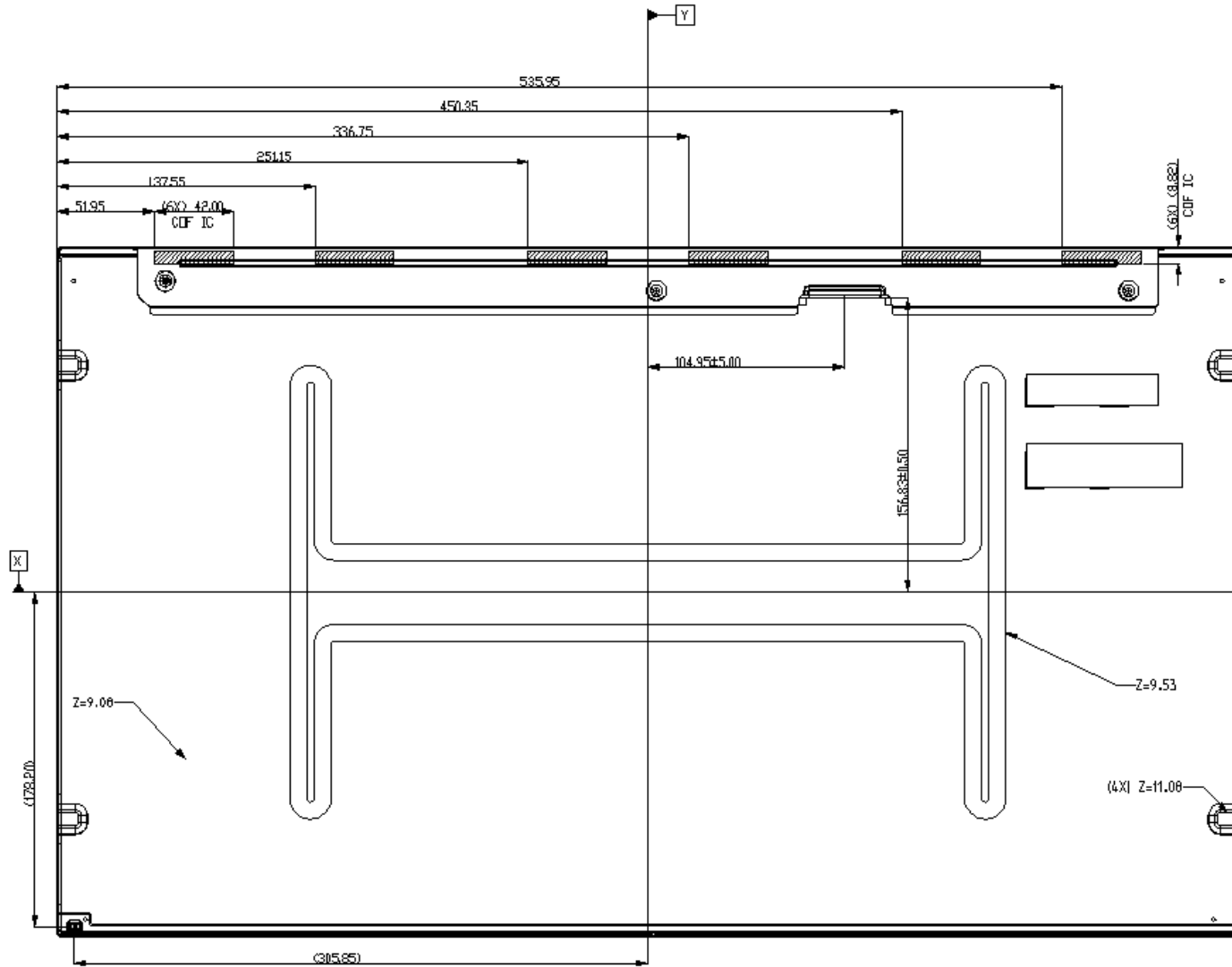
7 Mechanical Characteristics



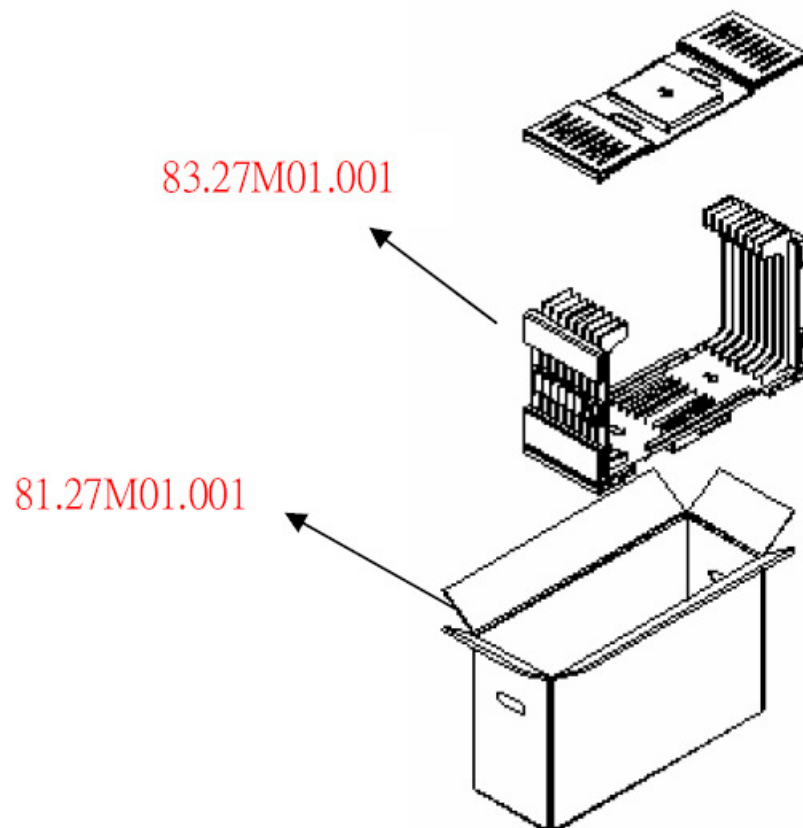
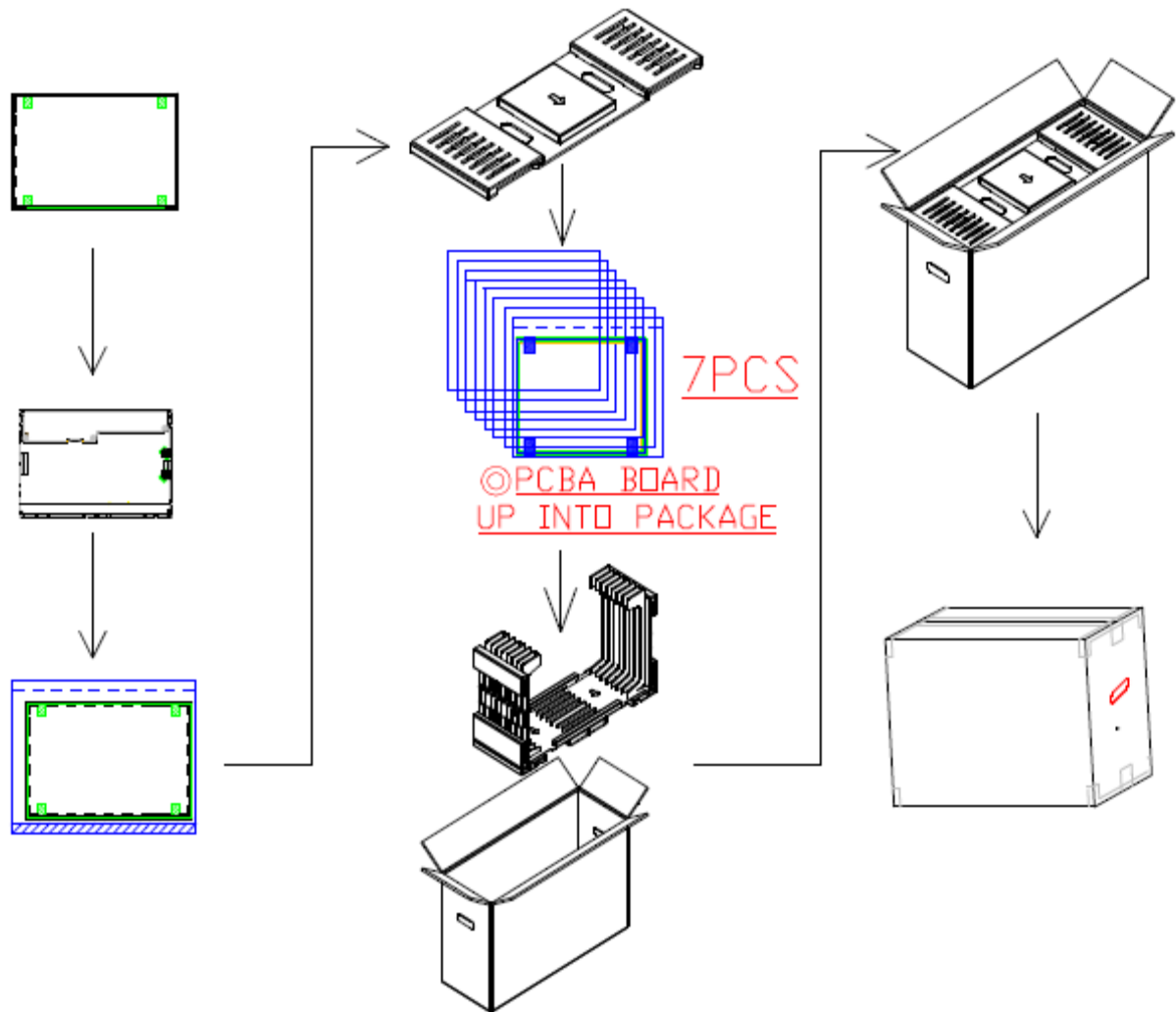
Avoid touching COF position when doing mechanical design



Avoid Touching CDF Position When Doing Mechanical Design



8 Packing Specification



Pallet and shipment information

	Item	Specification			Remark
		Q'ty	Dimension	Weight(kg)	
1	Panel	1	630(H)mm x 368.2(V)mm x 14.1(D)mm	2.86	
2	Cushion	1	-	2.97	
3	Box	1	720(L)mm x 264(W)mm x 455(H)mm	1.43	without Panel & cushion
4	Packing Box	7 pcs/Box	720(L)mm x 264(W)mm x 455(H)mm	26.80	with panel & cushion
5	Pallet	1	1070(L)mm x 740(W)mm x 140(H)mm	12	
6	Pallet after Packing	8 boxes/pallet	1070(L)mm x 740(W)mm x 140(H)mm	262.48	